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Applicant: Hiroshi Sakayori

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C. Willis
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Serial No.: 10/054,139

Filed: January 22, 2002

For: APPARATUS FOR TESTING INTEGRATED CIRCUITS

Art Unit: 2858

Examiner: Not yet assigned

Attorney Docket: 40010348-02

Commissioner for Patents
Washington, D.C. 20231

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INFORMATION DISCLOSURE STATEMENT

MAY 14 2002

Dear Sir:

TECHNOLOGY CENTER 2900

In accordance with applicant's duty of disclosure under 37 C.F.R. §1.56, please find attached hereto form PTO-1449 listing information which may be material to the patentability of this application, filed on January 22, 2002. This Information Disclosure Statement is being filed:

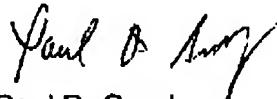
- Within three (3) months of the filing date of the national application;
- Within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;
- Before the mailing date of a first Office Action on the merits;
- After the filing date or date of first Office Action, but before the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p);
- After the filing date or date of first Office Action, but before the mailing date of a Notice of Allowance under 37 C.F.R. §1.311, provided that this occurs prior to the final action and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p);
- After the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e), a petition requesting consideration of the I.D.S., and the petition fee set forth in 37 C.F.R. §1.17(i)(1); and

After the mailing date of a Notice of Allowance under 37 C.F.R. §1.311, provided that this occurs prior to the issuance of a final action and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e), a petition requesting consideration of the I.D.S., and the petition fee set forth in 37 C.F.R. §1.17(l)(1).

Also enclosed is a copy of Japanese Article "Dynamic Test System for High Speed Mixed Signal Devices" and a Japanese article by Nikkei Electronics.

It should be understood that attention has been called to the references that have been deemed to be pertinent to the claimed present invention. In concluding what was pertinent, the criteria employed was considered most appropriate in light of the invention shown in the present application. However, the Examiner or others may deem some other criteria to be just as appropriate or more appropriate. Therefore, the Examiner is respectfully urged to review the listed references and to make the usual careful independent search for other prior art that may be pertinent.

Respectfully submitted,



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March 19, 2002

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